

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

Takeshi TAKAGI

Application No.: 10/657,799

Filed: September 09, 2003

For: SEMICONDUCTOR DEVICE



Customer Number: 20277

Confirmation Number: 1584

Group Art Unit: 2814

Examiner: Phat X. Cao

REQUEST FOR CORRECTED FILING RECEIPT

Mail Stop OIPE - Filing Receipt Correction
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Attached is a copy of the Filing Receipt received from the U.S. Patent and Trademark Office in the above-referenced application. It is noted that the Domestic Priority Information is incorrect. The Domestic Priority Information should read:

“This application is a CON of PCT/JP03/00472 01/21/2003”

It is requested that a corrected filing receipt be issued.

Respectfully submitted,

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APPL NO.	FILING OR 371 (c) DATE	ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT CLMS	IND CLMS
10/657,799	09/09/2003	2811	880	61352-043	17	15	2

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CONFIRMATION NO. 1584

UPDATED FILING RECEIPT



OC000000011870408

Date Mailed: 02/10/2004

Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Filing Receipt Corrections, facsimile number 703-746-9195. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

Applicant(s)

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Assignment For Published Patent Application

Matsushita Electric Industrial Co., LTD, Osaka, JAPAN;

Domestic Priority data as claimed by applicant

This application is a CIP of PCT/JP03/00472 01/21/2003

Foreign Applications

JAPAN 2002-011833 01/21/2002

If Required, Foreign Filing License Granted: 12/18/2003

Projected Publication Date: 05/20/2004

Non-Publication Request: No

Early Publication Request: No

Title

Semiconductor device

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FEB 12 2004

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Preliminary Class

257

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Title 37, Code of Federal Regulations, 5.11 & 5.15**

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